09/677263   Cofc

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September 12, 2005

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Certificate

OCT 1 9 2005

of Correction

     Re: **CERTIFICATE OF CORRECTION**
     U. S. Letters Patent No. 6,941,480
     Issued: September 6, 2005
     For: METHOD AND APPARATUS FOR
           TRANSITIONING A PROCESSOR STATE
           FROM A FIRST PERFORMANCE MODE TO
           A SECOND PERFORMANCE MODE
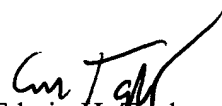   Inventor: Dai
     Our File No. 042390.P9724

Dear Sir:

     Enclosed is the Certificate of Correction (two copies) for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Edwin H. Taylor
Reg. No. 25,129

EHT/keh
Enclosures

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.   : 6,941,480 *B* |

DATED   : September 6, 2005

INVENTOR(S) : Dai

It is certified that error appears in the above-identified patent and that said Letters   Patent is hereby corrected as shown below:

In column 4, at line 56, delete "dock" and insert --clock--.

In column 5, at line 1, delete " dock" and insert --clock--.

In column 7, at line 29, delete "of".

In column 8, at line 35, delete "of".

                    Certificate of Correction (PTO Form 1050)-Amended

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

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DATED   : September 6, 2005

INVENTOR(S)   : Dai

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